

Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>			ATTORNEY DOCKET NO. AUS920030412US1		SERIAL NO. N t Assigned	
			APPLICANT Brown et al.			
			FILING DATE		GROUP ART UNIT Not Assign d	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
FOREIGN PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO	
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)						
TH	AA	Advanced Micro Devices White Paper, "Magic Packet Technology", Publication 20213, Revision A, Issued November 1995, pp. 1-6.				
TH	AB	http://www.maxim-ic.com/TINIdocs/networkedmicros.cfm , "Networked Microcontrollers, Solutions for Integrating Ethernet into Embedded Designs", 4/17/03, pp. 1-3.				
TH	AC	http://www.microsoft.com/hwdev/tech/network/netpm.asp , "Power Management for Network Devices", 4/17/03, pp. 1-8.				
TH	AD	http://msdn.microsoft.com/library/en-us/dndevice/html/netpm.asp?frame=true , "Power Management for Network Devices", 4/17/03, pp. 1-5.				
TH	AE	Intel 82550 Fast Ethernet Multifunction PCI/CardBus Controller, Product Brief, pp. 1-2.				
RELATED PATENT APPLICATIONS						
EXAMINER INITIAL	APPLICATION NO/ ATTY. DOCKET NO.	APPLICANT	TITLE		FILING DATE	
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